

PRELIMINARY CALL FOR PAPERS



LATS2022

23rd IEEE Latin-American Test Symposium

Montevideo, Uruguay, 5th - 8th September 2022

www.lats.ttc-events.org

23rd IEEE Latin-American Test Symposium

Montevideo, Uruguay, 5th - 8th September 2022



PRELIMINARY CALL FOR PAPERS

The IEEE Latin-American Test Symposium (LATS) is a recognized test and fault tolerance techniques forum attended by professionals from all over the world, in particular from Latin-America, to present and discuss various aspects of system, board, and component testing as well as design, manufacturing and in-field considerations with fault tolerance in mind. All presented papers are published in the IEEE Xplore Digital Library and the best papers of its 21st edition will be invited to re-submit to IEEE Design&Test, Journal of Electronic Testing: Theory and Applications (JETTA - Springer), and IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (TCAD).

Topics of interest include, but are not limited to

- Analog Mixed Signal Test
- Automatic Test Generation
- Built-In Self-Test
- Defect-Based Test
- Design and Synthesis for Testability
- Design for Electromagnetic Compatibility
- Design for Reliable Embedded Software
- Design Verification / Validation
- Economics of Test
- Fault Analysis and Diagnosis
- Fault Modeling and Simulation
- Fault-Tolerance in HW/SW
- Fault-Tolerant Architectures
- Memory Test and Repair
- On-Line Testing
- Process Control & Measurements
- Radiation / Electromagnetic Interference
- Hardening Techniques
- Software Fault-Tolerance
- Software On-Line Testing
- System-on-Chip Test
- Test Resource Partitioning
- Yield Optimization
- Hardware Security

Paper Submission Information

To encourage and facilitate discussions, participation will be limited. Those interested in presenting recent results at the symposium are invited to submit an extended abstract, one to three pages long, or a full length paper. PDF electronic submissions must be done via the symposium's webpage: www.lats.tttc-events.org

Authors should send papers in the IEEE conference format. Detailed instructions are available at the symposium's webpage. The Program Committee also welcomes proposals for panels and special sessions.

For additional information, please contact our Program Co-Chairs:

Leticia Bolzani Poehls – RWTH Aachen, Germany – leticia@poehls.com

Fabian Vargas – PUCRS, Brazil – vargas@computer.org

Submission Deadline (Title and Abstract): May 6th, 2022

Submission (Full paper): May 13th, 2022

Notification of Acceptance: June 24th, 2022

Camera Ready: July 15th, 2022.

Uruguay has 3.4 million inhabitants and almost half of the total population lives in the capital Montevideo. Even if its sights are not world famous, the city has a lot to offer: with its understatement the entire city exudes a zest for life and has special charm. The portal city of Montevideo is a great destination to combine a leisurely stroll past colonial buildings, parks and cafes with beautiful beaches and vibrating culture. Its streets can be explored by feet and are full of surprises - parks, churches, museums as well as shops, restaurants and cafes. The friendly and relaxed people will make you feel at ease and welcome in Montevideo!

Technical Sponsors:



The Institute of
Electrical and Electronics
Engineering, Inc.



Test Technology
Technical Council

General Co-Chairs:
Raoul Velazco - TIMA, France
raoul.velazco@univ-grenoble-alpes.fr

Yervant Zorian – SYNOPSIS, USA
yervant.zorian@synopsys.com

Program Co-Chairs:
Leticia Bolzani Poehls – RWTH Aachen, Germany
Fabian Vargas – PUCRS, Brazil

Embedded Tutorial Chair
Matteo Sonza Reorda - Politecnico di Torino, Italy

Photos from Montevideo:



Plaza Independencia



Aerial view of the city's coastline



Restaurant at the central market

Steering Committee:

Victor Champac - INAOE, Mexico
Marcelo Lubazewski - UFRGS, Brazil
Fabian Vargas - PUCRS, Brazil
Raoul Velazco - TIMA, France
Yervant Zorian - Synopsys, USA
Tiago Balen - UFRGS, Brazil
Leticia M. Bolzani Pöhls - PUCRS, Brazil
Matteo Sonza Reorda - Politecnico di Torino, Italy